Received Sep 03 02
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FORM PTO-1449	ATTY. DOCKET NO. 1778.0100002	APPLICATION NO. 09/662,832
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	APPLICANT Van Hook et al.	
	FILING DATE September 15, 2000	GROUP 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA5						
	AB5						
	AC5						
	AD5						
	AE5						
	AF5						
	AG5						
	AH5						
✓	AI5	5,671,401	09/1997	Harrell	345	505	
✓	AJ5	6,035,316	03/2000	Peleg et al.	708	523	
	AK5						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL5						Yes No
	AM5						Yes No
	AN5						Yes No
	AO5						Yes No
	AP5						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	5	
	AS	5	
	AT	5	

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

SKGF_OC1:50251.2

		ATTY. DOCKET NO. 1778.0100002	APPLICATION NO. 09/662,832
FORM PTO-1449		APPLICANT: Timothy J. Van HOOK et al.	
<u>INFORMATION DISCLOSURE STATEMENT</u>		FILING DATE: September 15, 2000	GROUP: 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA9						
	AB9						
	AC9						
	AD9						
	AE9						
	AF9						
	AG9						
	AH9						
	AI9						
	AJ9						
	AK9						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL9						Yes No
	AM9						Yes No
	AN9						Yes No
	AO9						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

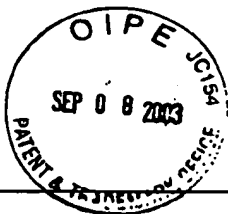|			
⋀	AN	2	*Piccolo - The ARM Signal Processing Architecture* [online], undated [retrieved on 2001-03-06] retrieved from the Internet: <URL http://www.calab.cs.pusan.ac.kr/ TechnicalReport/Advanced%20Computer%20Arch.../picolo.html>; pp 1-12.
	AO	2	
	AP	2	

EXAMINER		DATE CONSIDERED	

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SKGF Rev. 1/95

FORM PTO-1449

THIRD SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1778.0100002	APPLICATION NO. 09/662,832
APPLICANTS van Hook et al.	
FILING DATE September 15, 2000	GROUP 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
☑	AA1	4,511,990	04/1985	Hagiwara et al.			
☑	AB1	4,879,676	11/1989	Hansen			
☑	AC1	5,185,713	2/9/93	Kobunaya			
☑	AD1	5,392,228	02/1995	Burgess et al.			
☑	AE1	5,517,438	05/1996	Dao-Trong et al.			
☑	AF1	5,550,768	08/1996	Ogilvie et al.			
☑	AG1	5,726,927	03/1998	Wolrich et al.			09/11/1995
☑	AH1	5,729,724	03/1998	Sharangpani et al.			12/20/1995
☑	AI1	5,774,709	06/1998	Worrell			12/06/1995

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ						Yes No
	AK						Y s No
	AL						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

☑	AM	1	Gwennap, L., "UltraSparc Adds Multimedia Instructions," *Microprocessor Report*, MicroDesign Resources, Vol. 8, No. 16, December 5, 1994, pages 1-3.
☑	AN	1	Gwennap, L., "Intel's MMX Speeds Multimedia," *Microprocessor Report*, MicroDesign Resources, Vol. 10, No. 3, March 5, 1996, pages 1-6.
☑	AO	1	Heinrich, J., *MIPS R4000 Microprocessor User's Manual*, Second Edition, MIPS Technologies Inc., 1994, Pages 154-155, 157, 159, 161, 168, 170-171, B-9-10, B-13, B-17, B-19, B-21, B-23, B-27, B-38, B-40, and B-62.
☑	AP	1	*IEEE Standard for Binary Floating-Point Arithmetic*, IEEE, 1985, pages i-vi and 1-14.
☑	AQ	1	*TMS32010 User's Guide*, Texas Instruments Incorporated, 1983, page 1183.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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FORM PTO-1449		ATTY. DOCKET NO. 1778.0100002		APPLICATION NO. 09/662,832		
THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT		APPLICANTS van Hook et al.				
		FILING DATE September 15, 2000		GROUP 2183		

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
✓	AA2	5,790,827	08/1998	Leung			06/20/1997
✓	AB2	5,809,294	09/1998	Ando			01/30/1996
✓	AC2	5,815,695	09/1998	James et al.			06/07/1995
✓	AD2	5,848,269	12/1998	Hara			08/11/1997
✓	AE2	5,852,726	12/1998	Lin et al.			12/19/1995
✓	AF2	5,867,682	02/1999	Witt et al.			02/09/1996
✓	AG2	5,880,984	03/1999	Burchfiel et al.			01/13/1997
✓	AH2	5,953,241	09/1999	Hansen et al.			05/16/1997
✓	AI2	6,065,115	05/2000	Sharangpani et al.			04/10/1998

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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ						Yes No
	AK						Yes No
	AL						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

✓	AM	2	VIS™ Instruction Set User's Manual, Sun Microsystems, Inc., 1997, pages i-xii and 1-136, http://www.nondot.org/sabre/os/files/processors/VISInstructionSetUsersManual.pdf.
	AN		
	AO		
	AP		
	AQ		

EXAMINER	DATE CONSIDERED

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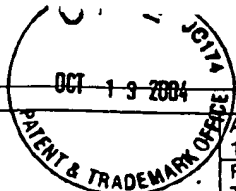
::ODMA\MHODMA\SKGF_DC1;154450;1

FORM PTO-1449

SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1778.0100002	APPLICATION NO. 09/662,832
FIRST NAMED INVENTOR Timothy J. Van Hook	
FILING DATE September 15, 2000	ART UNIT 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
h	AA1	4,809,212	02/1989	New *et al.*			
h	AB1	4,928,223	05/1990	Dao *et al.*			
h	AC1	5,499,299	03/1996	Takenaka *et al.*			
h	AD1	5,748,979	05/1998	Trimberger			06/07/1995
h	AE1	6,035,317	03/2000	Guy			01/08/1998
h	AF1	6,067,615	05/2000	Upton			11/09/1995
h	AG1	6,078,941	06/2000	Jiang *et al.*			12/10/1998
h	AH1	6,128,726	10/2000	LeComec			06/04/1996
h	AI1	6,141,421	10/2000	Takaragi *et al.*			12/08/1997
h	AJ1	6,154,834	11/2000	Neal *et al.*			05/27/1997
h	AK1	6,233,597 B1	05/2001	Tanoue *et al.*			07/07/1998

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1						Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

(initial)	AR	1	IBM Technical Disclosure Bulletin, *Pipelined Hardware Multiplier with Extended Precision*, Vol. 23, Issue No. 9, pp. 4322-4323, February 1981 (5 pages submitted).
	AS	1	
	AT	1	

EXAMINER		DATE CONSIDERED

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SKGF Rev. 12/03/03

FORM PTO-1449	ATTY. DOCKET NO. 1778.0100002	APPLICATION NO. 09/662,832
	FIRST NAMED INVENTOR Timothy J. Van Hook	
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	FILING DATE September 15, 2000	ART UNIT 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
✓	AA2	6,349,318 B1	02/2002	Vanstone et al.			10/14/1999
	AB2						
	AC2						
	AD2						
	AE2						
	AF2						
	AG2						
	AH2						
	AI2						
	AJ2						
	AK2						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	2	
	AS	2	
	AT	2	

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SKGF Rev. 12/03/03

FORM PTO-1449				ATTY. DOCKET NO. 1778.0100002 (0055.20US)			APPLICATION NO. 09/662,832	
INFORMATION DISCLOSURE STATEMENT				APPLICANT Van Hook et al.				
All references considered				FILING DATE September 15, 2000			GROUP 2183	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
W	AA3	5,581,773	12/1996	Glover	395	800	
W	AB3	5,590,345	12/1996	Barker et al.	395	800	
	AC3	5,666,298	09/1997	Peleg et al.	364	715.08	
	AD3	5,669,010	09/1997	Duluk, Jr.	395	800.22	
	AE3	5,721,892	02/1998	Peleg et al.	395	562	
	AF3	5,734,874	03/1998	Van Hook et al.	395	513	
W	AG3	5,740,340	04/1998	Purcell et al.	395	118	
	AH3	5,752,071	05/1998	Tubbs et al.	395	800.34	
	AI3	5,758,176	05/1998	Agarwal et al.	395	800	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ3						Yes No
	AK3						Yes No
	AL3						Yes No
	AM3						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

W	AN	3	Peleg, A. and Weiser, U., "MMX Technology Extension to the Intel Architecture," *IEEE Micro*, IEEE, pp. 42-50 (August 1996).
W	AO	3	*QML-DSP/MCM and Die Courier*, at ~~http://www~~ti.com/sc/docs/military/liter/ecour/dsp.htp, Texas Instruments, Inc., 7 pages (January 1996).
W	AP	3	Turley, J. and Hakkarainen, H., "TI's New 'C6x DSP Screams at 1,600 MIPS," *Microprocessor Report*, MicroDesign Resources, pp. 14-17 (February 17, 1997).
W	AQ	3	*DSP56002 24-Bit Digital Signal Processor Semiconductor Technical Data*, Motorola, Inc., 110 pages (1996). (Bates Numbers L07803-L07912).
W	AR	3	*A236 Parallel Digital Signal Processor Chip Programmer's Reference Manual*, Oxford Micro Devices, Inc., 195 pages (1994). (Bates Numbers L11184-L11378).

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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FORM PTO-1449	ATTY. DOCKET NO. 1778.0100002 (0055.20US)	APPLICATION NO. 09/662,832
	APPLICANT Van Hook et al.	
INFORMATION DISCLOSURE STATEMENT *All references considered.*	FILING DATE September 15, 2000	GROUP 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA4	5,761,523	06/1998	Wilkinson et al.	395	800.2	
	AB4	5,778,241	07/1998	Bindloss et al.	395	800.2	
	AC4	5,784,602	07/1998	Glass et al.	395	561	
	AD4	5,793,661	08/1998	Dulong et al.	364	750.5	
	AE4	5,812,147	09/1998	Van Hook et al.	345	511	
	AF4	5,812,723	09/1998	Ohtsu et al.	385	128	
	AG4	5,815,723	09/1998	Wilkinson et al.	395	800.2	
	AH4	5,822,606	10/1998	Morton	395	800.16	
	AI4	5,838,984	11/1998	Nguyen et al.	395	800.05	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ4						Yes No
	AK4						Yes No
	AL4						Yes No
	AM4						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	4	Lee, R., "Accelerating Multimedia with Enhanced Microprocessors," *IEEE Micro*, IEEE, pp. 22-32 (April 1995). (Bates Numbers L08566-L08576).
	AO	4	*DSP56000 24-Bit Digital Signal Processor Family Manual*, Motorola, Inc., 638 pages (1995). (Bates Numbers L08722-L09359).
	AP	4	*Microprocessor Devices Reliability Monitor Report: Third Quarter 1997*, Motorola, 8 pages (1997).
	AQ	4	*Reliability Monitor Environmental Stress Data 3Q97*, Motorola, 4 pages (1997).
	AR	4	*Reliability and Quality Report 3Q97*, Motorola, 2 pages (1997).

EXAMINER		DATE CONSIDERED

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FORM PTO-1449		ATTY. DOCKET NO. 1778.0100002 (0055.20US)		APPLICATION NO. 09/662,832			
INFORMATION DISCLOSURE STATEMENT		APPLICANT Van Hook et al.					
All references considered.		FILING DATE September 15, 2000		GROUP 2183			

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
[initials]	AA5	5,864,703	01/1999	van Hook et al.	395	800.22	
[initials]	AB5	5,881,307	03/1999	Park et al.	395	800.23	
[initials]	AC5	5,936,872	08/1999	Fischer et al.	364	754.03	
[initials]	AD5	5,960,012	09/1999	Spracklen	371	53	
[initials]	AE5	5,996,056	11/1999	Volkonsky	712	1	
[initials]	AF5	6,006,316	12/1999	Dinkjian	712	22	
[initials]	AG5	6,058,465	05/2000	Nguyen	712	7	
[initials]	AH5	6,088,783	07/2000	Morton	712	22	
	AI5						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ5						Yes No
	AK5						Yes No
	AL5						Yes No
	AM5						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

[initials]	AN	5	*Microprocessor Devices Reliability Monitor Report: Fourth Quarter 1997*, Motorola, 10 pages (1997).
[initials]	AO	5	*Reliability and Quality Report 4Q97*, Motorola, 2 pages (1997).
[initials]	AP	5	"Hardware Implications of *xmem* as a *st* followed by a *ld*," *Motorola Semiconductor Engineering Bulletin*, Motorola, 5 pages (September 1992).
[initials]	AQ	5	*601 First Silicon*, at http:/www.mot.com/SPS/PowerPC/library/press_releases/601_First_Silicon.html, Motorola, 2 Pages (October 1, 1992).
[initials]	AR	5	*DSP56002/DSP56L002 24-Bit Digital Signal Processor Motorola Semiconductor Product Information*, Motorola, Inc., 3 pages (1994). (Bates Numbers L07913-L07915).

EXAMINER		DATE CONSIDERED	

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	FORM PTO-1449		ATTY. DOCKET NO. 1778.0100002 (0055.20US)		APPLICATION NO. 09/662,832	
INFORMATION DISCLOSURE STATEMENT			APPLICANT Van Hook et al.			
All references considered			FILING DATE September 15, 2000		GROUP 2183	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA6						
	AB6						
	AC6						
	AD6						
	AE6						
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	AG6						
	AH6						
	AI6						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AJ6						Yes No
	AK6						Yes No
	AL6						Yes No
	AM6						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

✓	AN	6	MC88410UM/AD: MC88410 Secondary Cache Controller User's Manual, at http://www.mot-sps.com/lit/html/mc88410umad.html, Motorola, Inc., 1 page (January 24, 1997).
✓	AO	6	AN1214: MC88110 64-bit External Bus Interface to 16-bit EPROM, at http://www.mot-sps.com/lit/html/an1214.html, Motorola, Inc., 1 page (January 24, 1997).
✓	AP	6	TMS320C80 Digital Signal Processor, Texas Instruments Corp., p. 41 (July 1994).
✓	AQ	6	EB162: Programming Tips (MC88110), at http://www.mot-sps.com/lit/html/eb162.html, Motorola, Inc., 1 page (January 24, 1997).
✓	AR	6	MC88110/410DH/AD: MC88110/MC88410 Designer's Handbook, at http://www.mot-sps.com/lit/html/mc88110410dhad.html, Motorola, Inc., 1 page (January 24, 1997).

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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		ATTY. DOCKET NO. 1778.0100002 (0055.20US)	APPLICATION NO. 09/662,832
FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT *All references considered.*		APPLICANT Van Hook *et al.*	
		FILING DATE September 15, 2000	GROUP 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA7						
	AB7						
	AC7						
	AD7						
	AE7						
	AF7						
	AG7						
	AH7						
	AI7						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ7						Yes No
	AK7						Yes No
	AL7						Yes No
	AM7						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

✓	AN	7	*MC88110UMAD: MC88110 Second Generation RISC Microprocessor User's Manual*, at http://www.mot-sps.com/lit/html/mc88110umad.html, Motorola, Inc., 1 page (January 24, 1997).
✓	AO	7	Weiss, R., *ARM Piccolo Mixes ARM RISC with DSP*, at http://ww.estd.com/Editorial/1996/11/Briefs/arm.html, 1 page, (November 1996).
✓	AP	7	*TMS320C1x/C2x/C2xx/C5x Assembly Language Tools User's Guide*, Texas Instruments, 483 pages (March 1995). (Bates Numbers L07916-L08398).
✓	AQ	7	*TMS320C5x General-Purpose Applications User's Guide*, Texas Instruments, 167 pages (July 1997). (Bates Numbers L08399-L08565).
✓	AR	7	El-Sharkawy, Mohamed, Ph.D., *Digital Signal Processing Applications with Motorola's DSP56002 Processor*, Prentice Hall PTR, pp. 43-124 (1996). (Bates Numbers L06519-L06601).

EXAMINER		DATE CONSIDERED	

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::ODMA\WHODMA\SKGF_DC1;11089;2

FORM PTO-1449		ATTY. DOCKET NO. 1778.0100002 (0055.20US)			APPLICATION NO. 09/662,832		
INFORMATION DISCLOSURE STATEMENT *All references considered.*		APPLICANT Van Hook et al.					
		FILING DATE September 15, 2000			GROUP 2183		

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA8						
	AB8						
	AC8						
	AD8						
	AE8						
	AF8						
	AG8						
	AH8						
	AI8						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ8						Yes No
	AK8						Yes No
	AL8						Yes No
	AM8						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

✓	AN	8	Killian, E., "MIPS Extensions for Digital Media," *Microprocessor Forum*, pp. 1- 5 (October 22-23, 1996).
✓	AO	8	"Interrupt Latency in the MC88110," *Motorola Semiconductor Engineering Bulletin*, Motorola, pp. 1, 2 and 4-9 (1992).
✓	AP	8	"Running the MC88110 in Lockstep," *Motorola Semiconductor Engineering Bulletin*, Motorola, 2 pages (1992).
✓	AQ	8	Turley, J., "Siemens TriCore Revives CISC Techniques: New 32-Bit Design Emphasizes DSP Capability and Microcontroller Functions," *Microprocessor Report*, Microdesign Resources, pp. 13-16 (November 17, 1997).
✓	AR	8	*i860™ Microprocessor Family Programmer's Reference Manual*, Intel Corporation, 79 pages (1992). (Bates Numbers L09361-L09439).

EXAMINER		DATE CONSIDERED	

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